FIG. 1

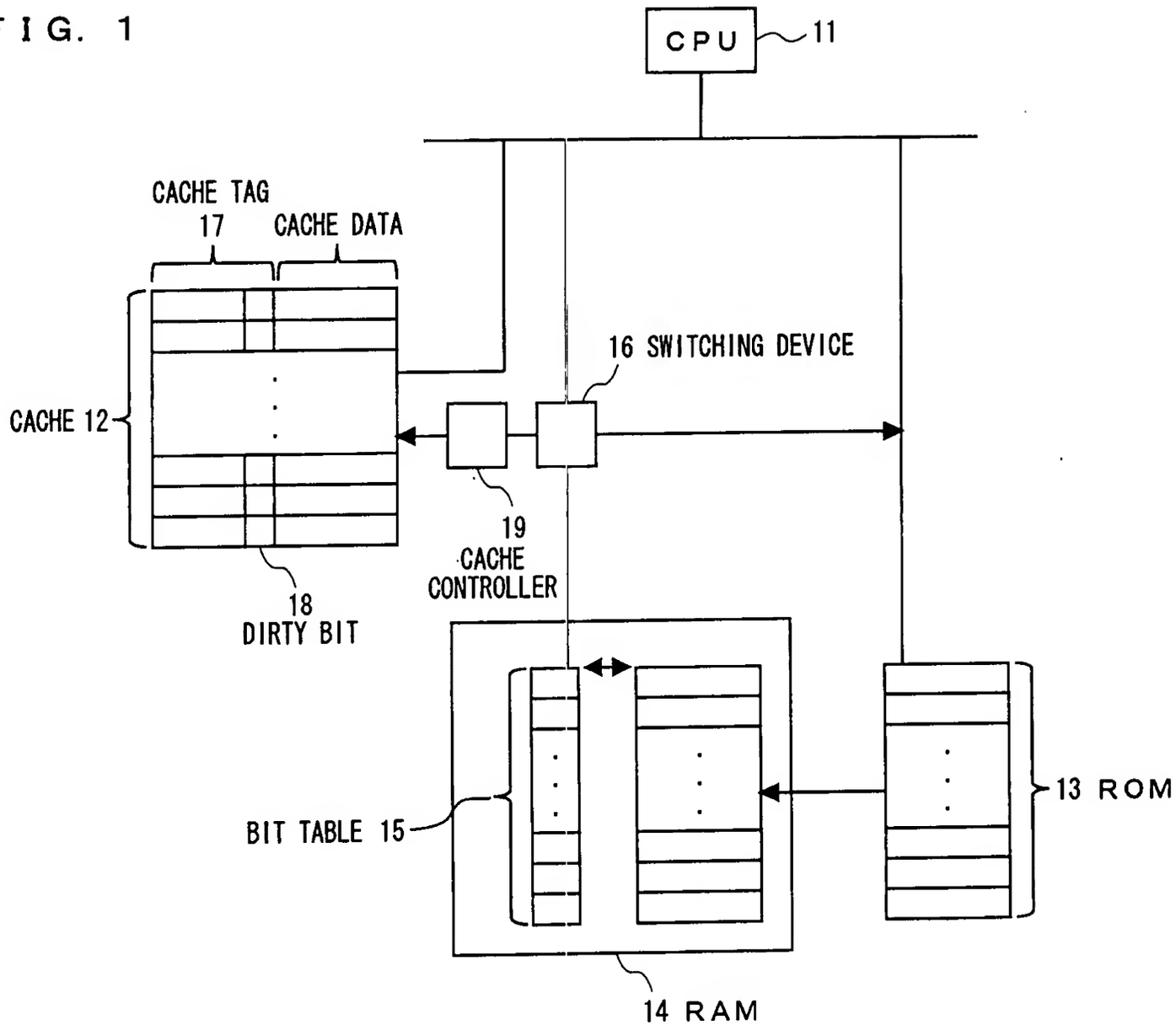
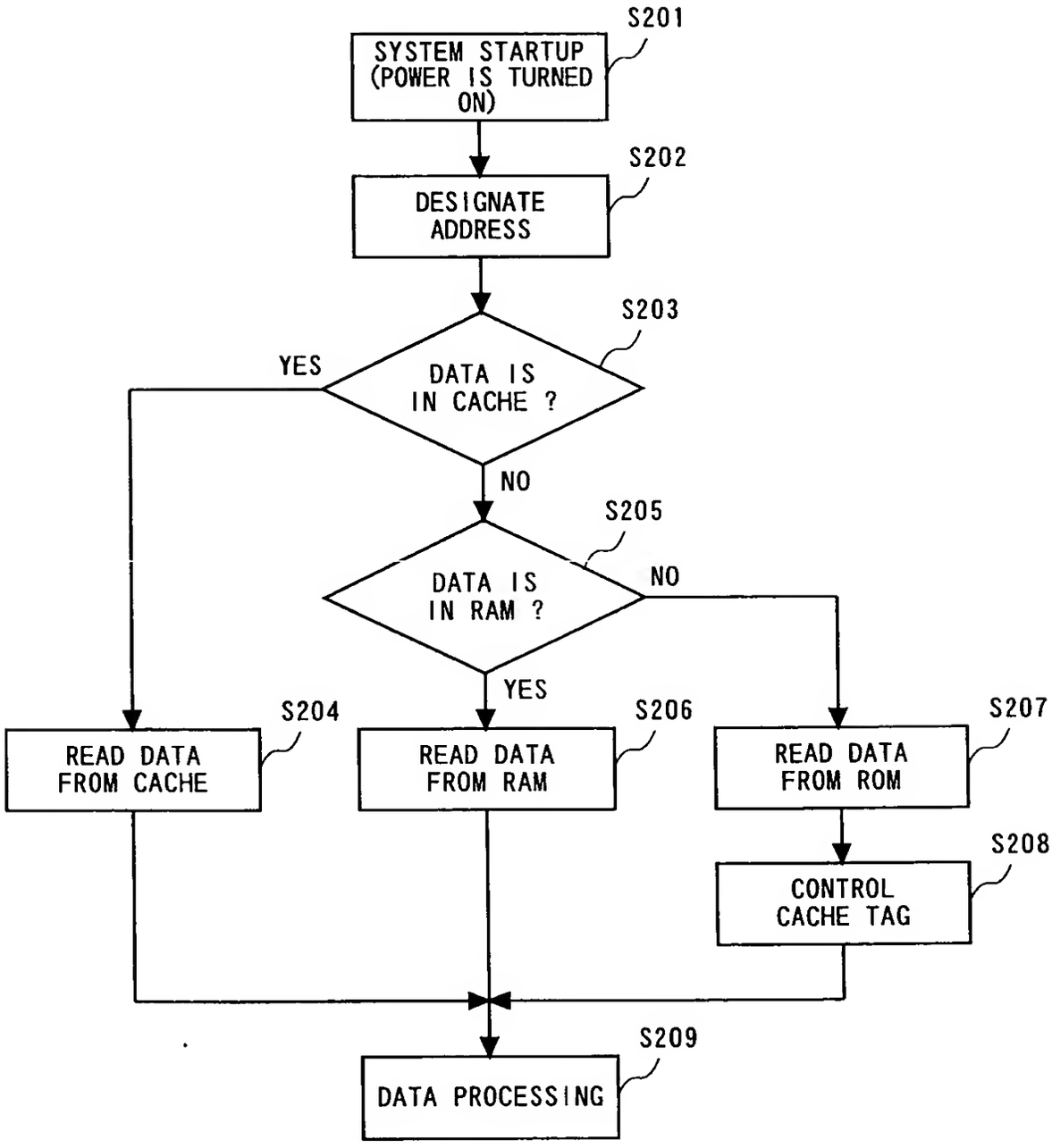

CPU
11
CACHE TAG
17
CACHE DATA
CACHE 12
16 SWITCHING DEVICE
19
CACHE
CONTROLLER
18
DIRTY BIT
BIT TABLE 15
13 ROM
14 RAM

FIG. 2


S201
SYSTEM STARTUP (POWER IS TURNED ON)
S202
DESIGNATE ADDRESS
S203
DATA IS IN CACHE ?
YES
NO
S205
DATA IS IN RAM ?
NO
YES
S204
READ DATA FROM CACHE
S206
READ DATA FROM RAM
S207
READ DATA FROM ROM
S208
CONTROL CACHE TAG
S209
DATA PROCESSING

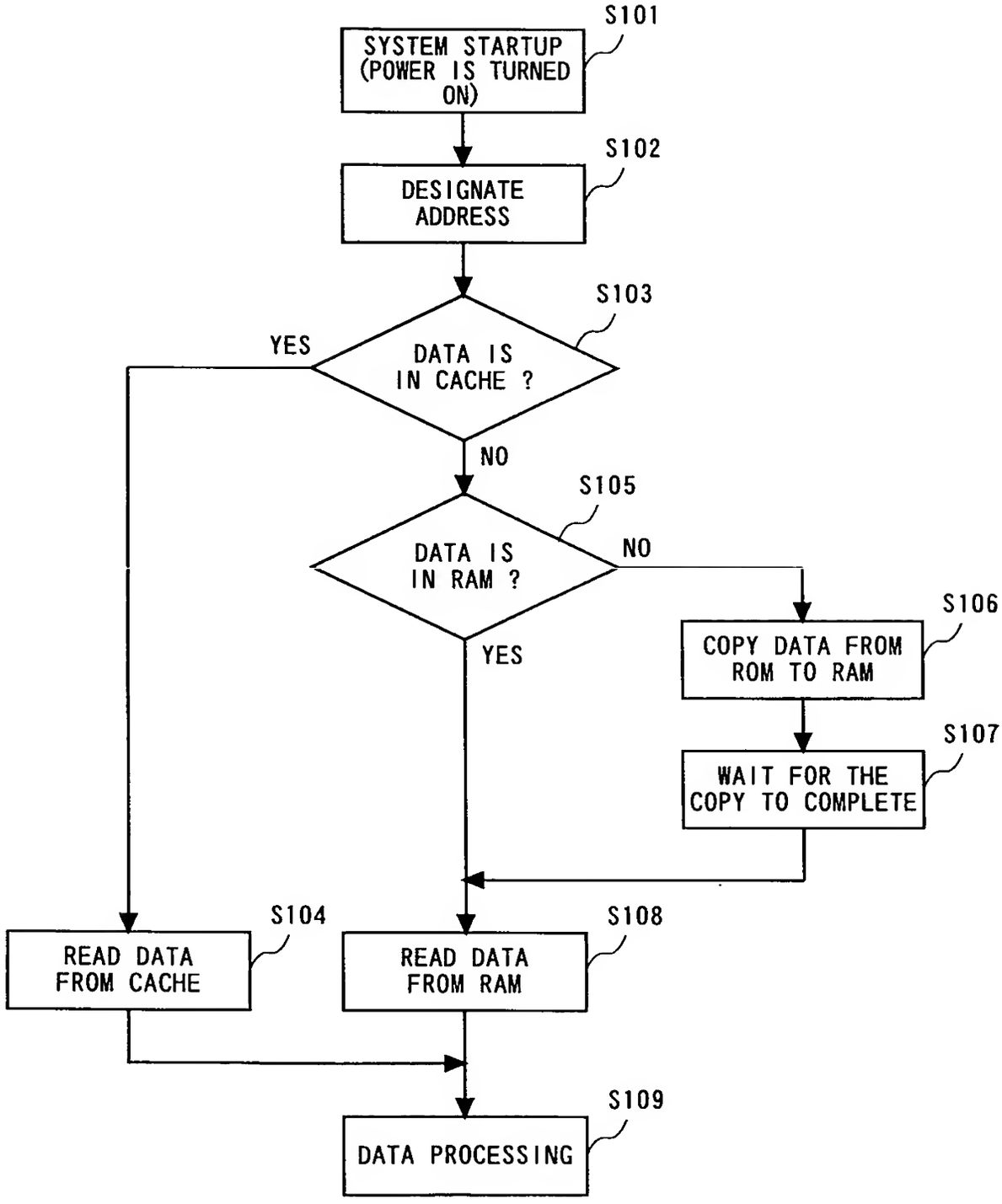



FIG. 3 PRIOR ART